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RESPONSE UNDER 37 C.F.R. 1.116 EXPEDITED PROCEDURE--EXAMINING GROUP 2142

et No. 5649-905

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Hong-Sik Jeong et al.

Serial No.: 10/008,700

Filed: December 7, 2001

Confirmation No.: 5150

Group Art Unit: 2818

Examiner: Chuong A. Luu

For: METHODS OF MANUFACTURING INTEGRATED CIRCUIT DEVICES

HAVING AN ENCAPSULATED INSULATION LAYER

NOTICE OF APPEAL TO THE BOARD OF PATENT APPEALS AND INTERFERENCES

Sir:

Pursuant to 35 USC § 134, 37 CFR § 41.31, and MPEP § 1205, the Applicant in the above-identified patent application hereby appeals to the Board of Patent Appeals and Interferences from the Office Action dated July 7, 2006 at least twice rejecting or finally rejecting claims 6-24.

| Enclosed is a check in the amount of \$500.00 for the Appeal fee as provided by 37 C.F.R. § 41.20(b)(1).

| Please first reapply any previously paid notice of appeal fee and appeal brief.

| Payment by credit card is requested. Form PTO-2038 is attached.

| A petition for an extension of time under 37 CFR § 1.136(a) is enclosed.

| Also enclosed: Request for pre-appeal brief review; Reasons in support of Applicants' pre-appeal brief request for review

| The Commissioner is authorized to charge any additional fees that may be

required or credit any overpayment to Deposit Account No. 50-0220.

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Registration No.: 42,01

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Certificate of Mailing under 37 CFR § 1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, V/22313-1450 on December 7, 2006.

Amelia Tauchen